Application No. 09/666,456 Reply to Office Action of June 11, 2003

IN THE SPECIFICATION

Please amend the paragraph at page 5, lines 29-33 to read as follows:

Preferably, the pipeline control unit stops execution of an instruction in the processor core in accordance with a second pipeline stop signal for suspending execution of an instruction, the second pipeline stop signal being inputted from the extended arithmetic unit and executed by the processor core.

0/2